



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Shahzad Khalid et al.
Title: Non-Volatile Memory and Method With Bit Line Compensation
Dependent on Neighboring Operating Modes
Application No.: 10/667,223 Filing Date: September 17, 2003
Examiner: Le, Toan K. Group Art Unit: 2824
Docket No.: SNDK.342US0 Conf. No.: 7018

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 3, 2005

Mary S. Briggs
Signature

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Commissioner for Patents
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AMENDMENT UNDER 37 C.F.R. §1.312

Sir:

Specification Amendments begin on page 2 of this paper.

Remarks begin on page 3 of this paper.